



X-1097-1D US  
10/618,039

PATENT  
Conf. No. 4121

IN THE UNITED STATES PATENT OFFICE

Applicant: Trevor J. Bauer  
Assignee: Xilinx, Inc.  
Title: Pass Gate Multiplexer Circuit with Reduced  
Susceptibility to Single Event Upsets  
Serial No.: 10/618,039 Filing Date: 7/11/2003  
Examiner: Terry Lee Englund Art Unit: 2816  
Docket No.: X-1097-1D US Conf. No.: 4121

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COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed February 27,  
2004, Applicant submits the following amendments and remarks.

**Amendments to the Abstract** are reflected on page 2 of this  
paper.

**Amendments to the Claims** are reflected in the listing of  
claims which begins on page 3 of this paper.

**Remarks** begin on page 5 of this paper.